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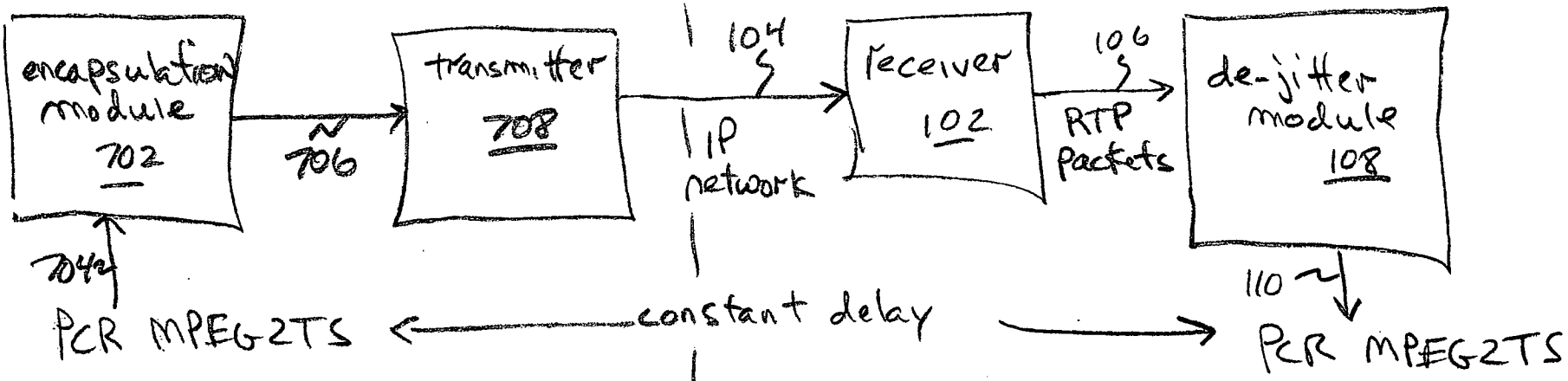


Fig. 7

100

Fig. 1

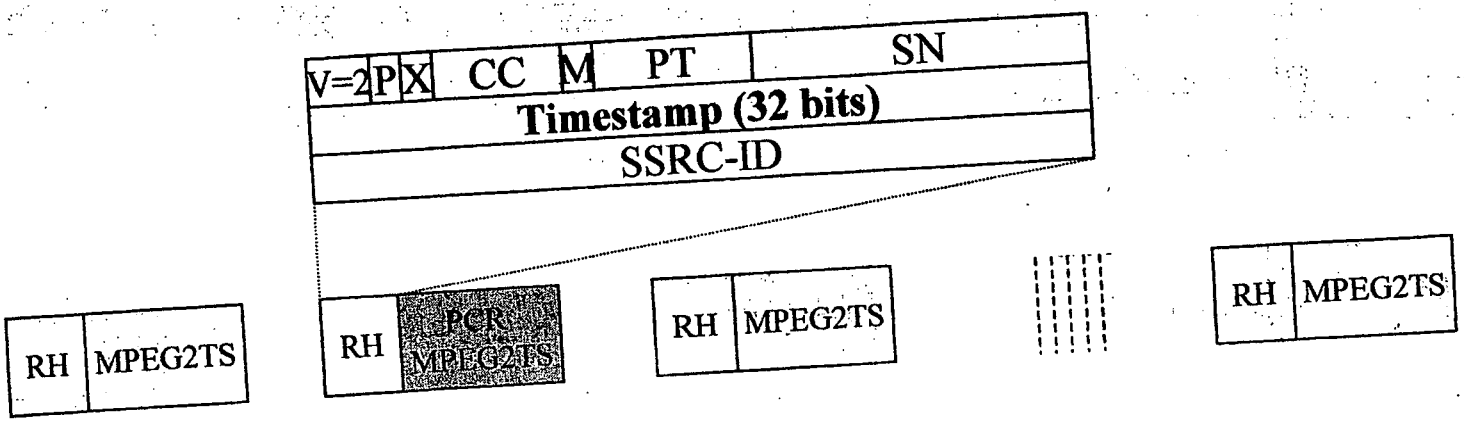

V=2
P
X
CC
M
PT
SN
Timestamp (32 bits)
SSRC-ID
RH
MPEG2TS
RH
PCR
MPEG2TS
RH
MPEG2TS
RH
MPEG2TS

Fig. 2

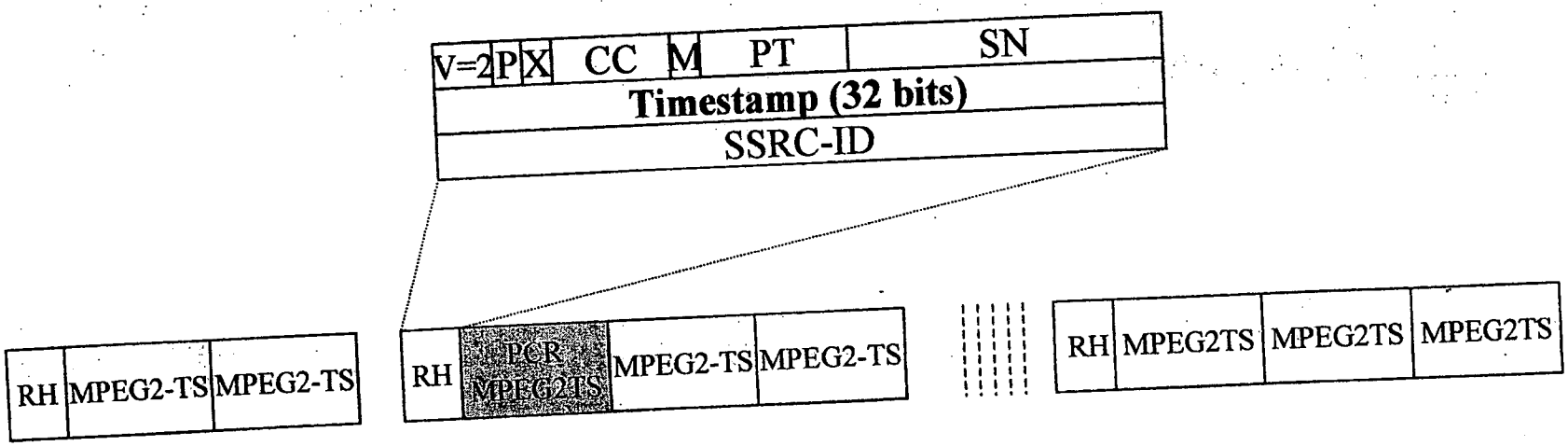

V=2
P
X
CC
M
PT
SN
Timestamp (32 bits)
SSRC-ID
RH
MPEG2-TS
MPEG2-TS
RH
PCR
MPEG2TS
MPEG2-TS
MPEG2-TS
RH
MPEG2TS
MPEG2TS
MPEG2TS

Fig. 3

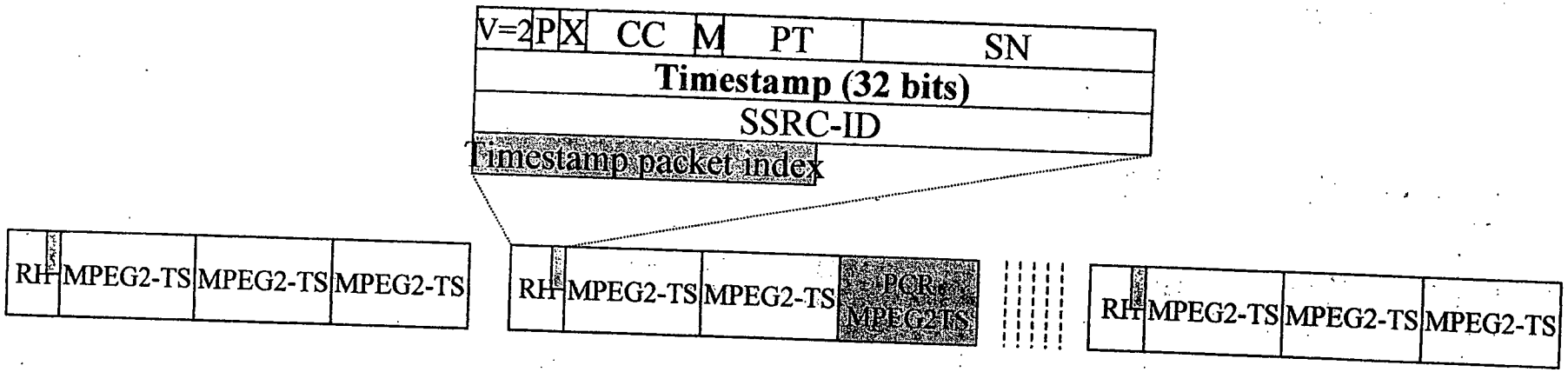

V=2
P
X
CC
M
PT
SN
Timestamp (32 bits)
SSRC-ID
Timestamp packet index
RH
MPEG2-TS
MPEG2-TS
MPEG2-TS
RH
MPEG2-TS
MPEG2-TS
PCR
MPEG2-TS
RH
MPEG2-TS
MPEG2-TS
MPEG2-TS

Fig. 4

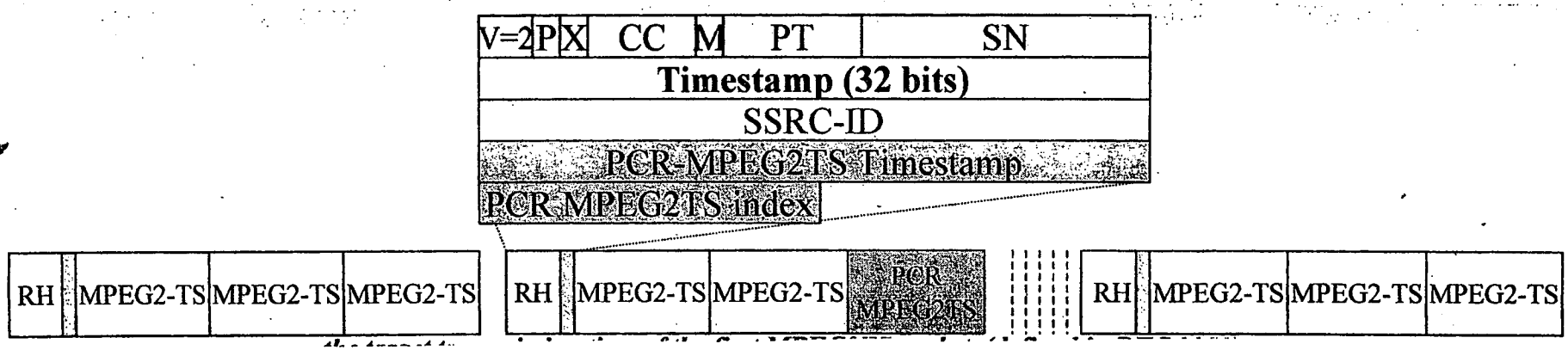
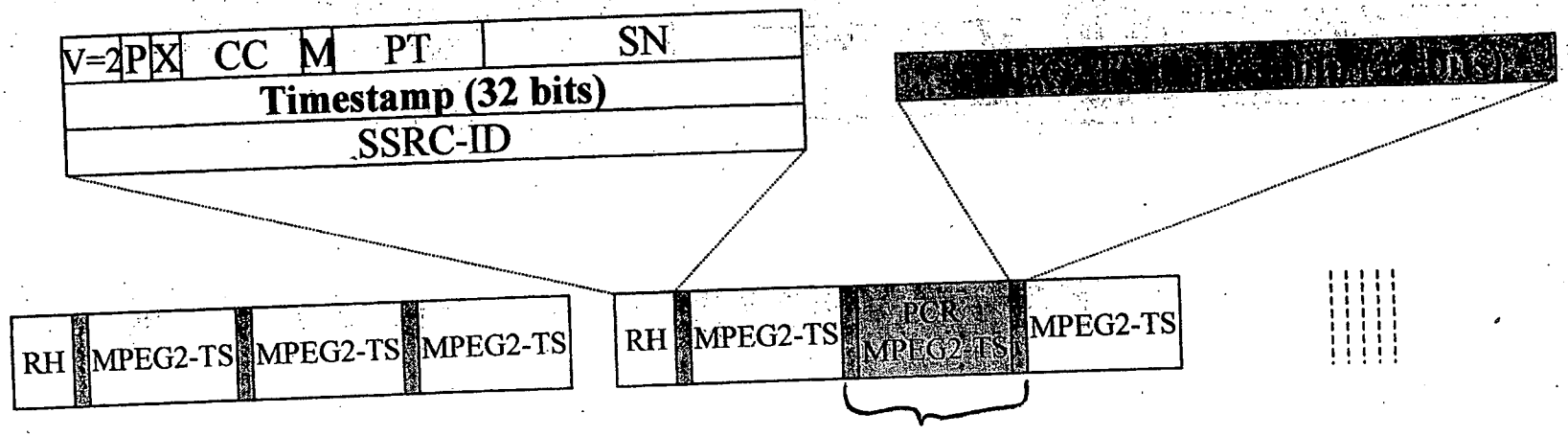

V=2
P
X
CC
M
PT
SN
Timestamp (32 bits)
SSRC-ID
PCR-MPEG2TS Timestamp
PCR-MPEG2TS index
RH
MPEG2-TS
MPEG2-TS
MPEG2-TS
RH
MPEG2-TS
MPEG2-TS
PCR MPEG2TS
RH
MPEG2-TS
MPEG2-TS
MPEG2-TS

Fig. 5



mpeG2TS
delay compensation
structure

Fig. 6

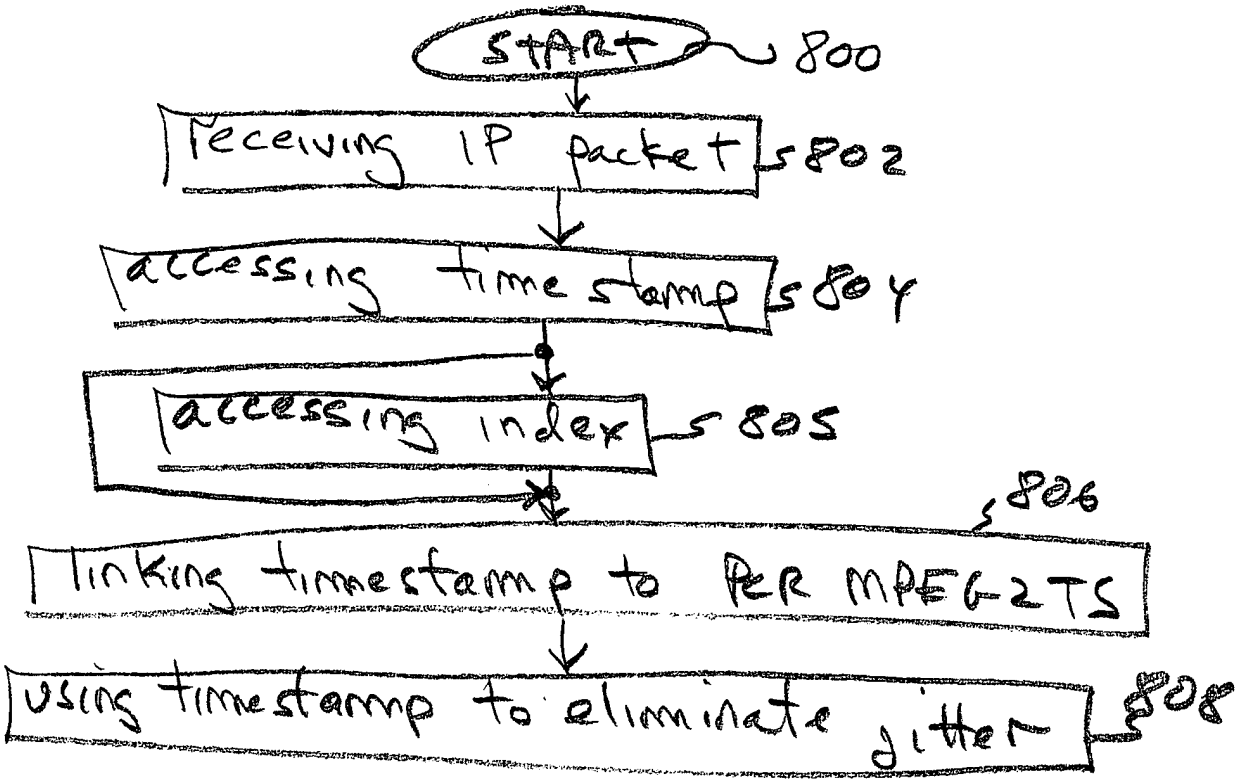

START
800
Receiving IP packet
802
accessing time stamp
804
accessing index
805
806
linking timestamp to PER MPEG-2 TS
using timestamp to eliminate jitter
808

Fig. 8

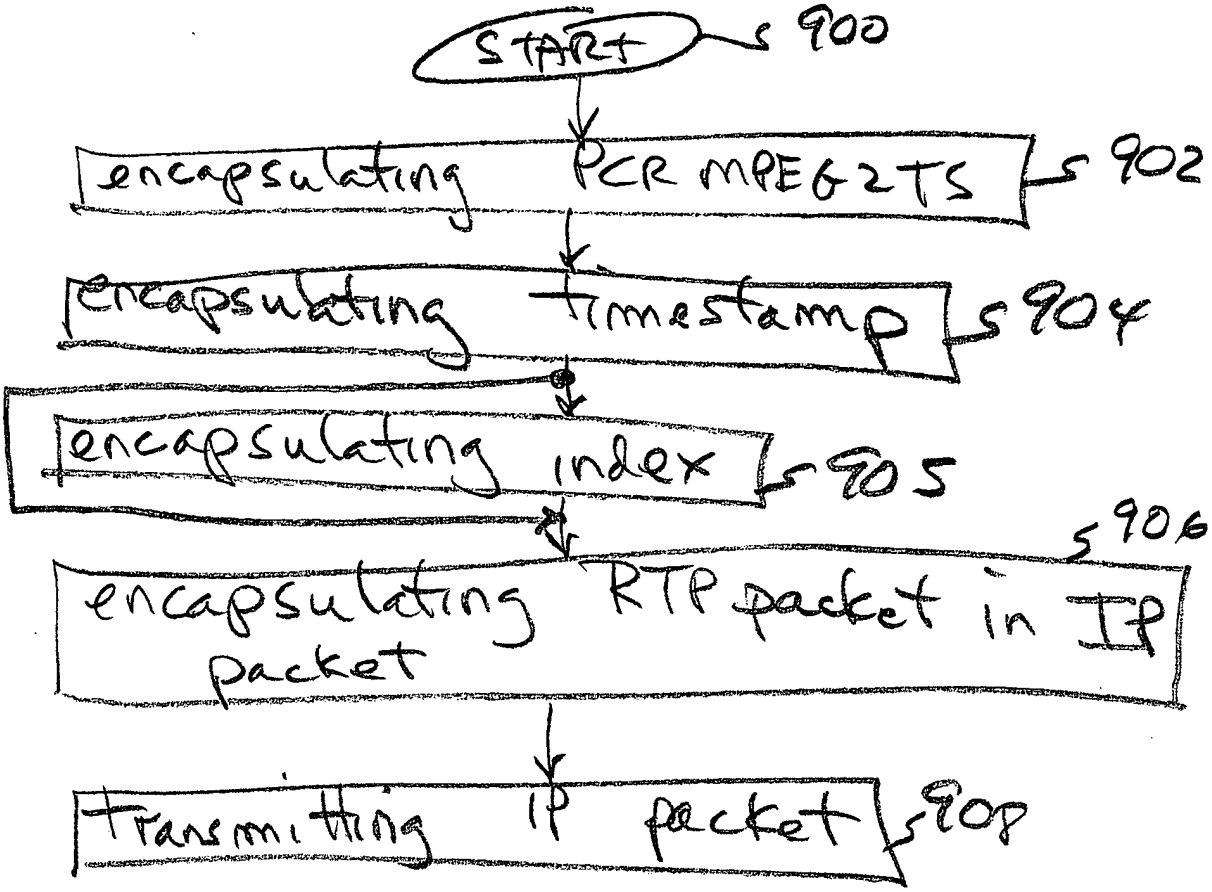
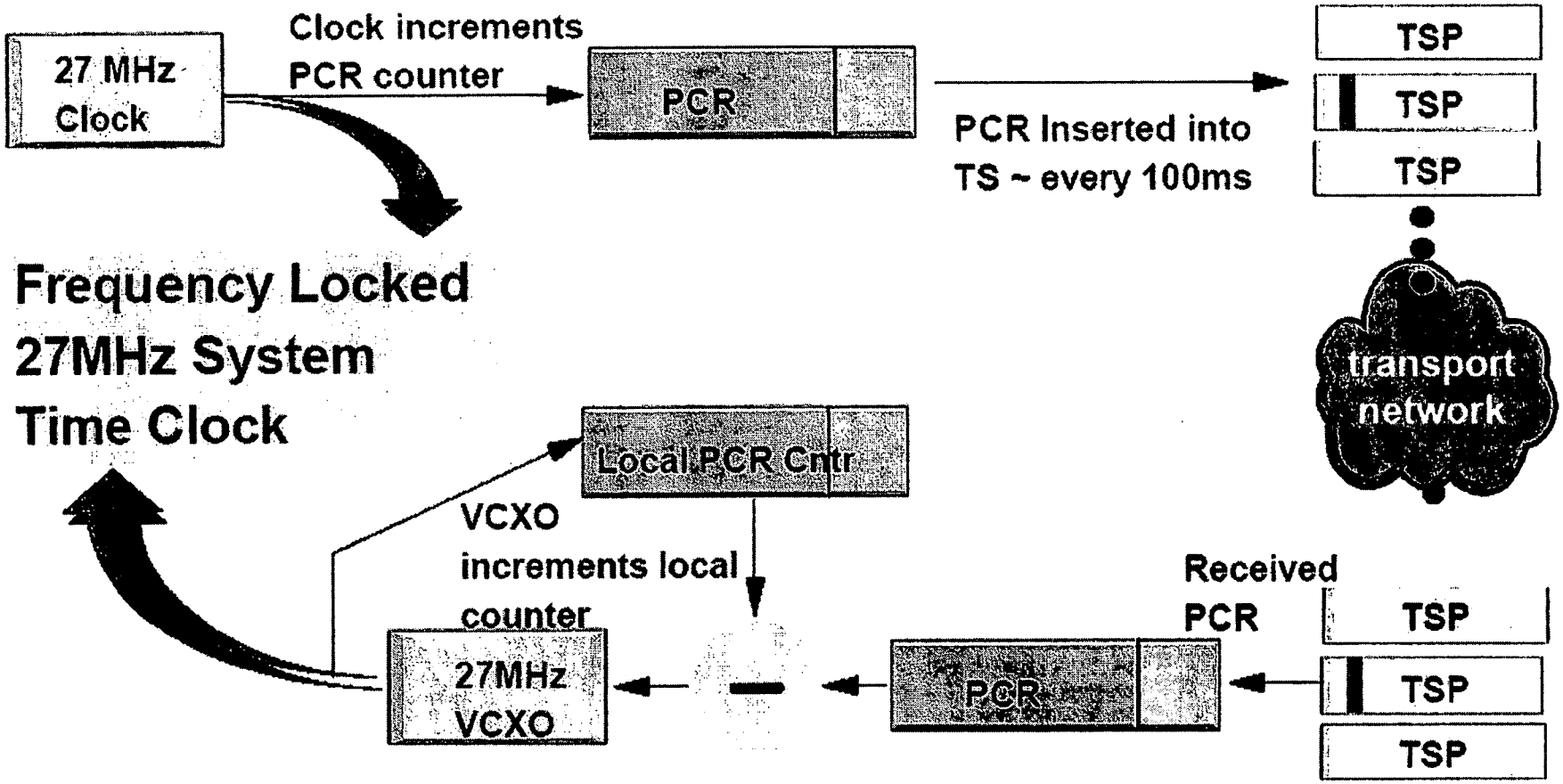

START
900
encapsulating PCR MPEG2TS
902
encapsulating timestamp
904
encapsulating index
905
906
encapsulating RTP packet in IP packet
transmitting IP packet
908

Fig. 9



System clock frequency = 27M hz +/- 540 hz

Fig. 10 (prior art)

Timing model of MPEG system

- From ISO/IEC 13818-1

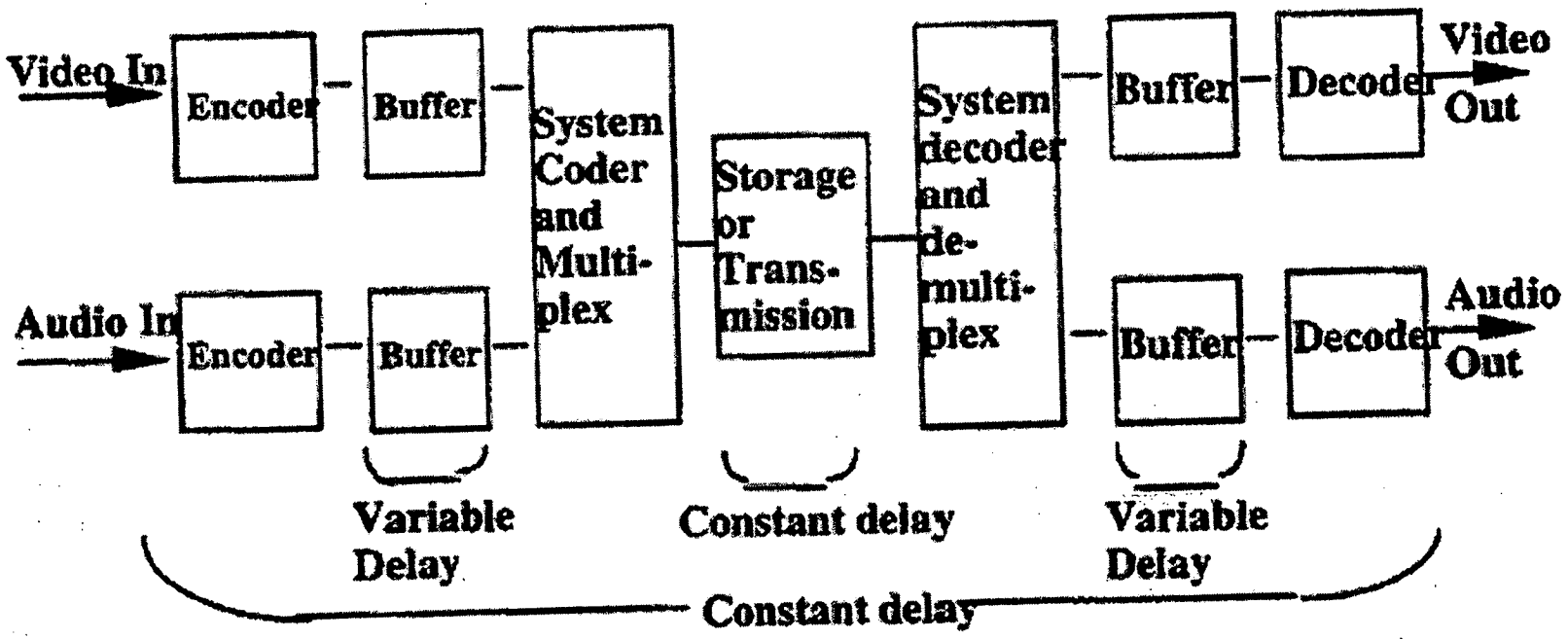
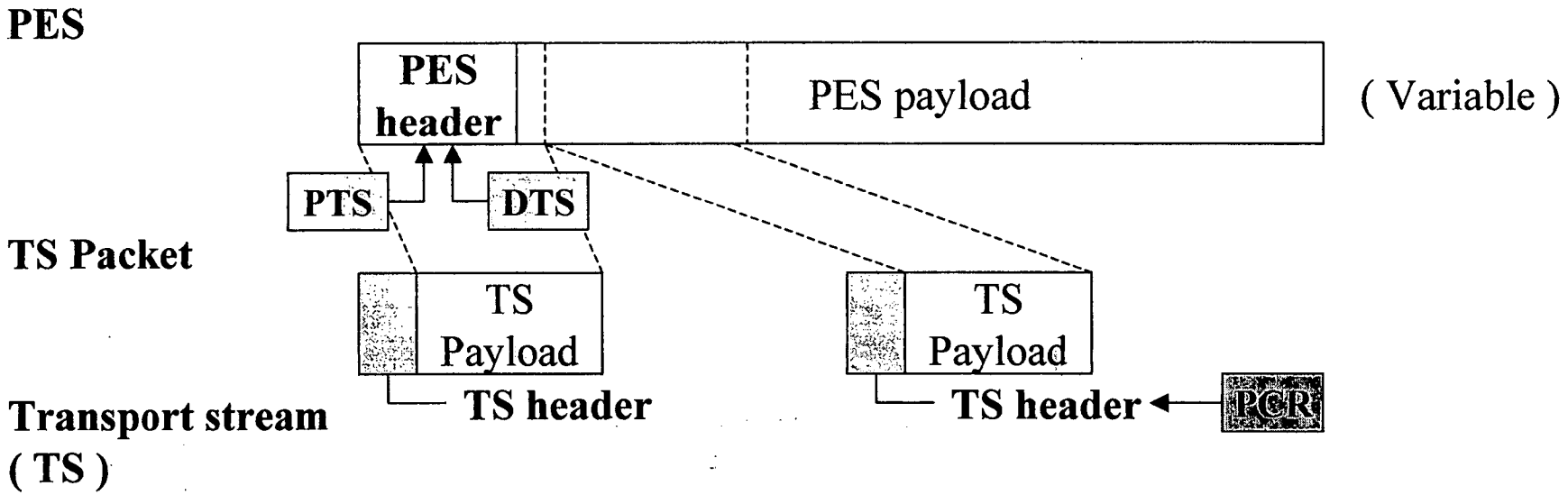


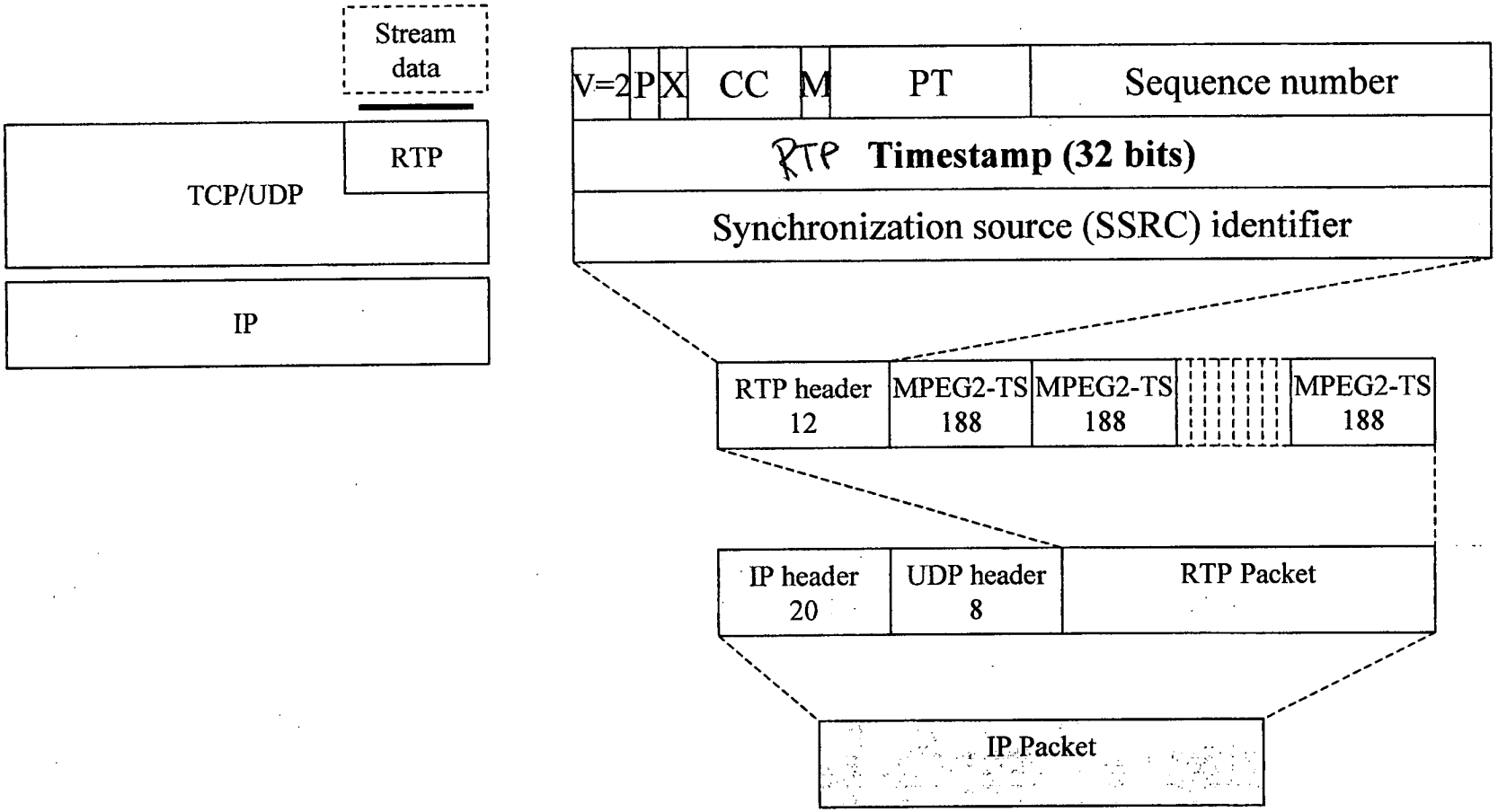
Fig. 11 (Prior art)

Fig. 12 (PRIOR ART)

MPEG2TS packet structure



ISO/IEC 13818 requires that *PCR information should be inserted to MPEG2TS packet at least once per 100ms. NOT in every TS packet.*
PES: packetized element stream

Fig. 13 (prior art)

Using RTP (as defined in RFC 2250)